

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: CHUNG, Yi Sun Conf.:
Appl. No.: New Group:
Filed: September 20, 2002 Examiner:
For: METHOD FOR FORMING AN ISOLATION REGION
IN A SEMICONDUCTOR DEVICE

COPY

L E T T E R

Assistant Commissioner for Patents
Washington, DC 20231

September 20, 2002

Sir:

Under the provisions of 35 U.S.C. § 119 and 37 C.F.R. § 1.55(a), the applicant(s) hereby claim(s) the right of priority based on the following application(s):

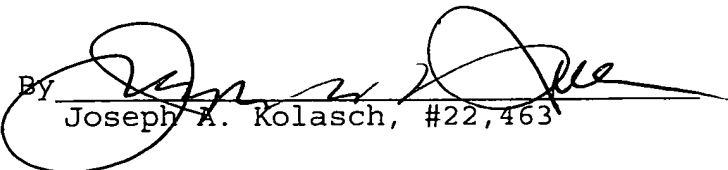
<u>Country</u>	<u>Application No.</u>	<u>Filed</u>
KOREA	2001-0058285	September 20, 2001

A certified copy of the above-noted application(s) is(are) attached hereto.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fee required under 37 C.F.R. §§ 1.16 or 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By 
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JAK/cqc
0465-0893P

Attachment

PSKB 703-205-8000
465-0893P
CHUNG
September 20, 2002
108-1

대한민국 특허청

KOREAN INTELLECTUAL PROPERTY OFFICE

별첨 사본은 아래 출원의 원본과 동일함을 증명함.

This is to certify that the following application annexed hereto
is a true copy from the records of the Korean Intellectual
Property Office.

출원번호 : 특허출원 2001년 제 58285 호
Application Number PATENT-2001-0058285

출원년월일 : 2001년 09월 20일
Date of Application SEP 20, 2001

출원인 : 주식회사 하이닉스반도체
Applicant(s) Hynix Semiconductor Inc.



2001 년 12 월 14 일

특 허 청

COMMISSIONER

